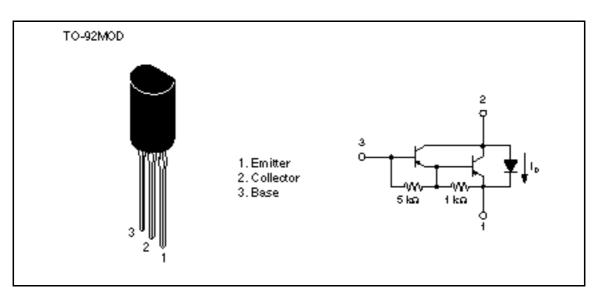
Silicon PNP Epitaxial, Darlington

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Application

- Low frequency power amplifier
- Complementary pair with 2SD1978

Outline





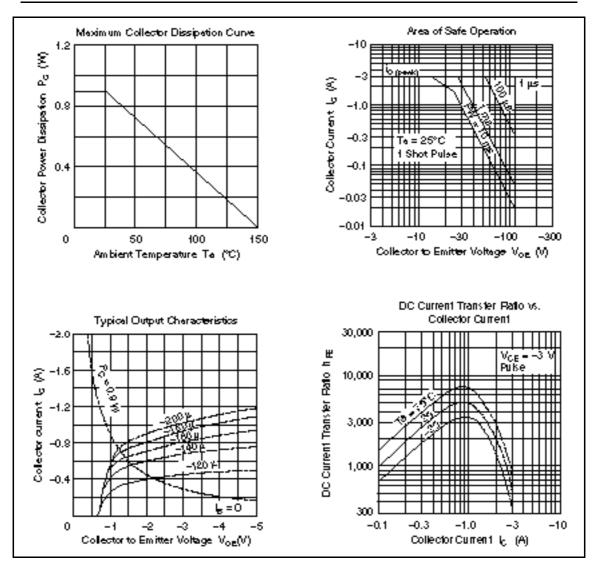
Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	-120	V
Collector to emitter voltage	V _{CEO}	-120	V
Emitter to base voltage	V _{EBO}	-7	V
Collector current	I _c	-1.5	А
Collector peak current	İ _{C(peak)}	-3.0	А
C to E diode forward current	I _D	1.5	А
Collector power dissipation	Pc	0.9	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

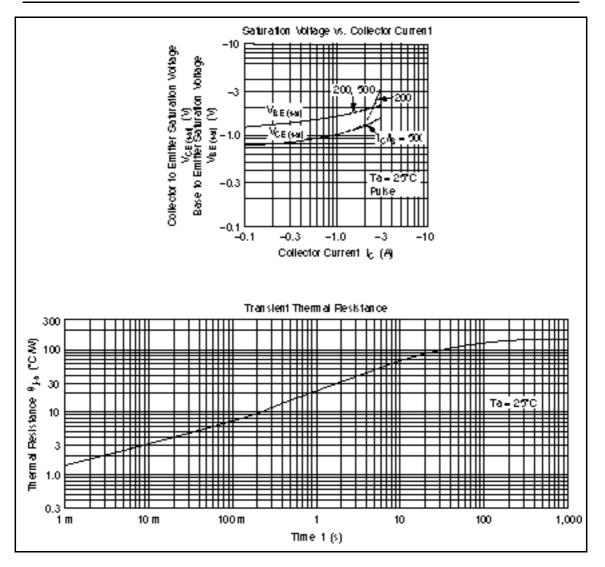
Electrical Characteristics (Ta = 25° C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{\rm (BR)CBO}$	-120	_	_	V	$I_{c} = -0.1 \text{ mA}, I_{e} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	-120	_	_	V	$I_{c} = -10 \text{ mA}, R_{BE} =$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	-7	_	_	V	$I_{\rm E} = -50$ mA, $I_{\rm C} = 0$
Collector cutoff current	I _{CBO}		_	-1	μA	$V_{CB} = -100 \text{ V}, I_{E} = 0$
	I _{CEO}			-10	μA	$V_{ce} = -100 \text{ V}, \text{ R}_{be} =$
DC current transfer ratio	h_{FE}	2000	_	10000		$V_{ce} = -3 \text{ V}, \text{ I}_{c} = -1 \text{ A}^{*1}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)1}}$	_	_	-1.5	V	$I_c = -1 \text{ A}, I_B = -1 \text{ mA}^{*1}$
	$V_{\text{CE(sat)2}}$	_	_	-2.0	V	$I_{c} = -1.5 \text{ A}, I_{B} = -1.5 \text{ mA}^{*1}$
Base to emitter saturation voltage	$V_{BE(sat)1}$	_	_	-2.0	V	$I_{\rm C} = -1$ A, $I_{\rm B} = -1$ mA ^{*1}
	$V_{\text{BE(sat)2}}$			-2.5	V	$I_{\rm C} = -1.5 \text{ A}, I_{\rm B} = -1.5 \text{ mA}^{*1}$
C to E diode forward voltage	V _D			3.0	V	$I_{\rm D} = 1.5 \ {\rm A}^{\star 1}$

Note: 1. Pulse test



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